Application No.: 10/043,104 Docket No.: M4065.0317/P317-B

AMENDMENTS TO THE CLAIMS

- 1. 11. (Previously Cancelled)
- 12. (Currently Amended) A semiconductor structure package comprising:

a substrate having an upper surface, <u>and</u> a lower surface, <u>and an edge region</u> disposed between said upper and lower surfaces, said edge including a first cut portion and a second broken portion comprising a sheared portion <u>one of said upper surface</u> and said lower surface including at least one groove capable of being broken to separate the substrate into a plurality of segments.

- 13. (Currently Amended) A semiconductor <u>structure package</u> as defined in claim 12 wherein said cut portion <u>at least one groove</u> further comprises a sawn portion.
- 14. (Currently Amended) A semiconductor <u>structure package</u> as defined in claim 12 wherein said cut portion <u>at least one groove</u> further comprises a scribed portion.
- 15. (Currently Amended) A semiconductor <u>structure</u> package as defined in claim 12 wherein said <u>cut portion</u> at least one groove further comprises a chemically etched portion.
- 16. (Currently Amended) A semiconductor <u>structure</u> package as defined in claim 12 wherein said <u>cut portion</u> at least one groove further comprises a milled portion.
 - 17–27. (Previously Cancelled)